

IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application.

1. (Currently Amended) A radio frequency tuner for analogically and digitally modulated signals, comprising

a tuner arrangement for converting any selected one of analog and digital channels to one

of baseband and zero intermediate frequency analog quadrature signals,

a low pass filter for filtering out the selected channel from said analog quadrature signals,

the low pass filter attenuating a first channel adjacent to the selected channel, the

low pass filter attenuating a second channel at an aliasing frequency of the

selected channel, the low pass filter correcting imbalances based on a feedback

signal, and the low pass filter being a quadrature anti-alias filter,

a dual analog-digital converter for converting said analog quadrature signals to digital quadrature signals, and

a digital signal processor coupled with the low pass filter for processing said digital

quadrature signals, said processing including performing channel filtering to pass

said selected channel and substantially to reject other channel signals, said

processor providing the feedback signal to the low pass filter.

2. (Original) A tuner as claimed in claim 1, comprising a remodulator for converting an analog channel processed by said processor to an analogically modulated signal at a predetermined intermediate frequency.

3. (Original) A tuner as claimed in claim 2, in which said predetermined intermediate frequency is a non-zero intermediate frequency.
4. (Original) A tuner as claimed in claim 2, in which said remodulator comprises a digital remodulator and a digital-analog converter.
5. (Original) A tuner as claimed in claim 1, in which said processor is arranged to correct quadrature conversion errors in said tuner arrangement.
6. (Original) A tuner as claimed in claim 1, comprising a dual analog anti-alias filter between said tuner arrangement and said analog-digital converter.
7. (Original) A tuner as claimed in claim 6, in which said anti-alias filter is a low pass filter.
8. (Original) A tuner as claimed in claim 6, in which said dual anti-alias filter is the only filtering between said tuner arrangement and said analog-digital converter.
9. (Original) A tuner as claimed in claim 1, in which said processor has selectable filtering characteristics for different modulation standards.

10. (Original) A tuner as claimed in claim 1, comprising a digital demodulator connected to said processor.

11. (Original) A tuner as claimed in claim 1, comprising an analog demodulator.

12. (Original) A tuner as claimed in claim 1, comprising a single monolithic integrated circuit.